

Figure 1.

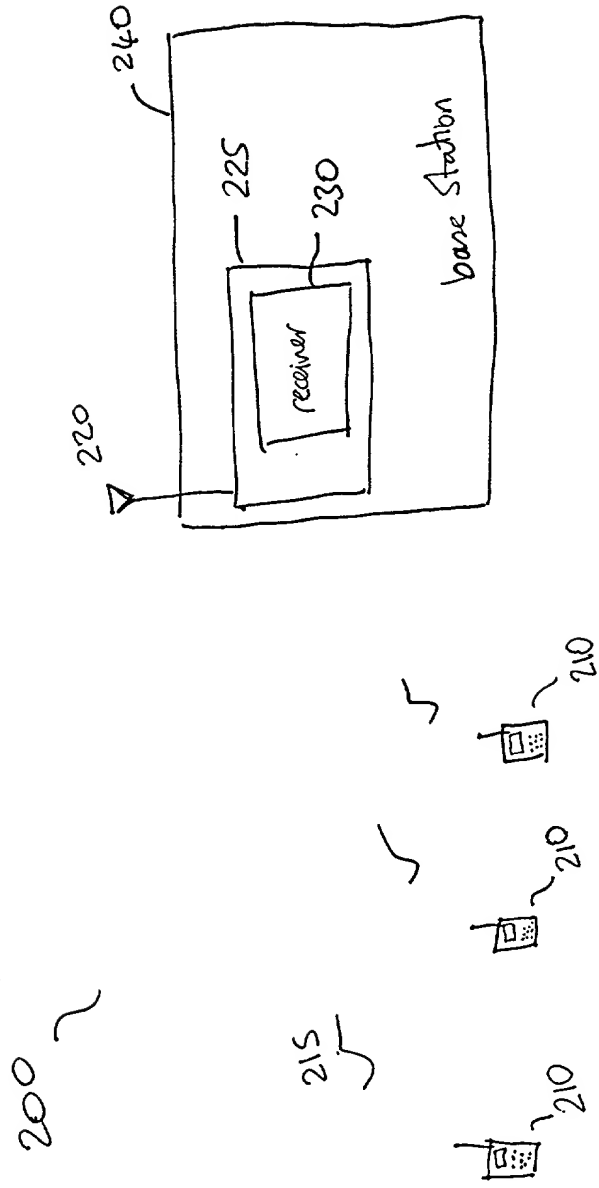


Figure 2.

FIG. 3 is a block diagram of a receiver system.

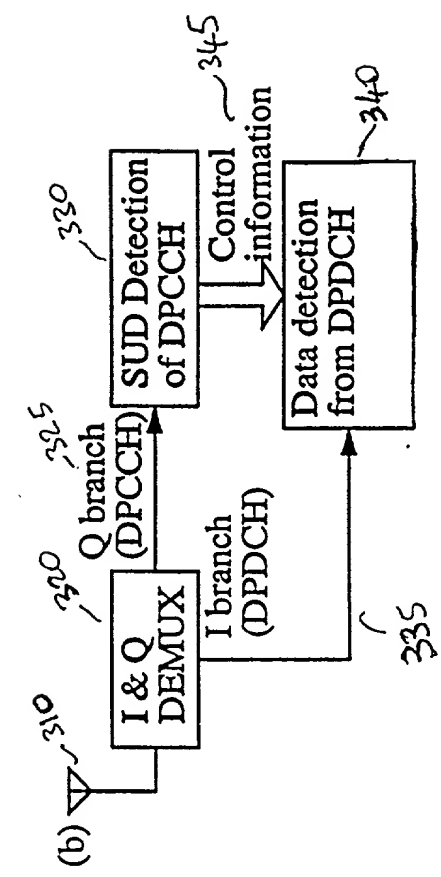


Figure 3.

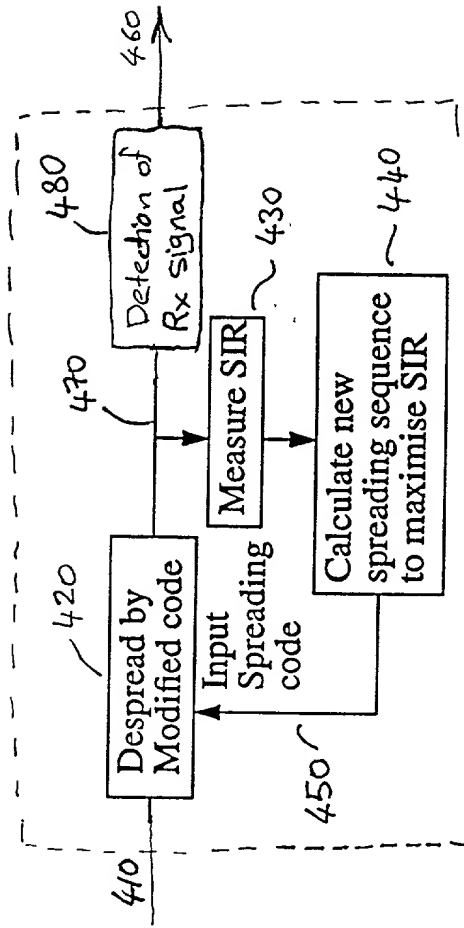


Figure 4

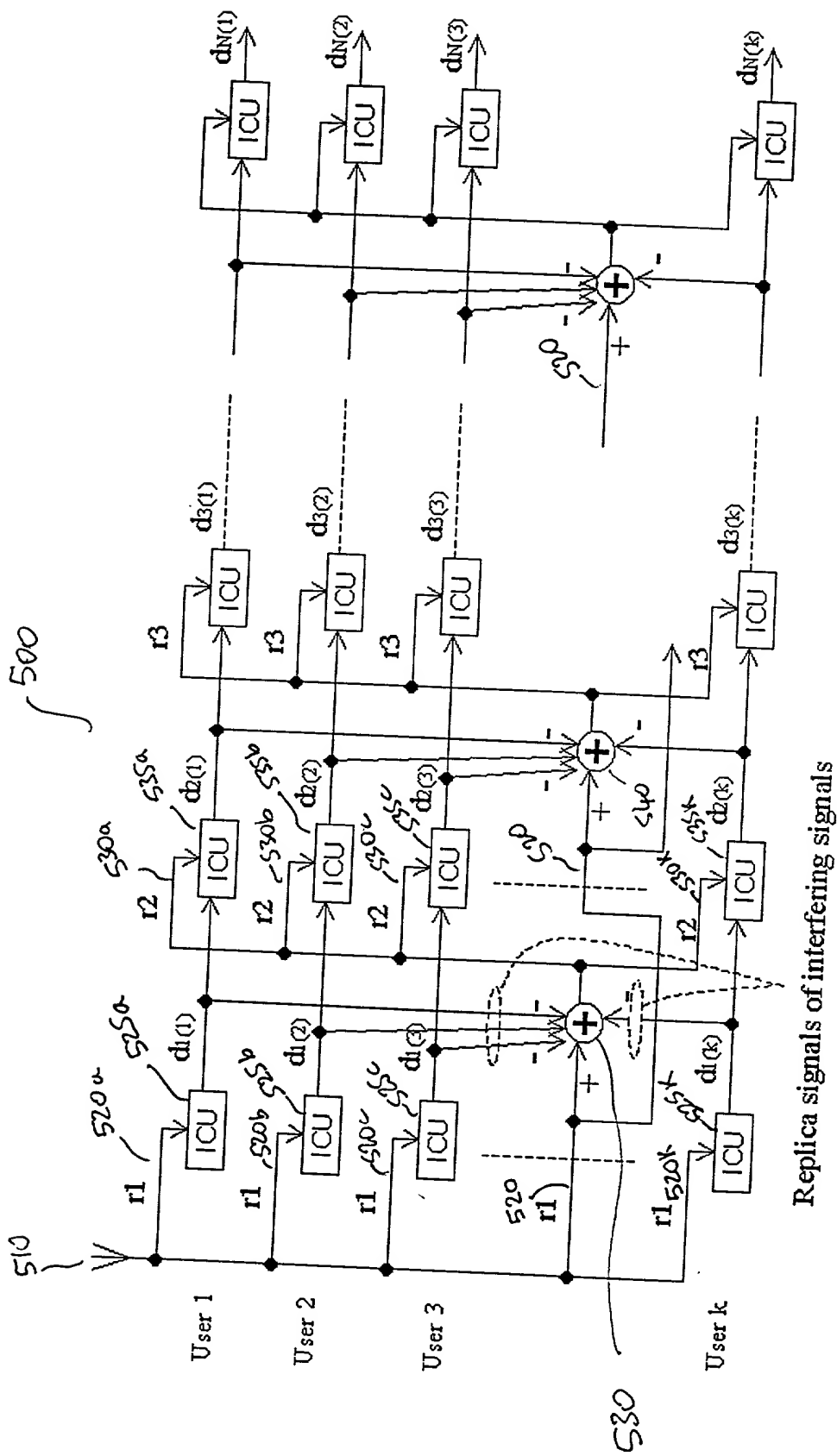


Fig 5

600

Channel estimation

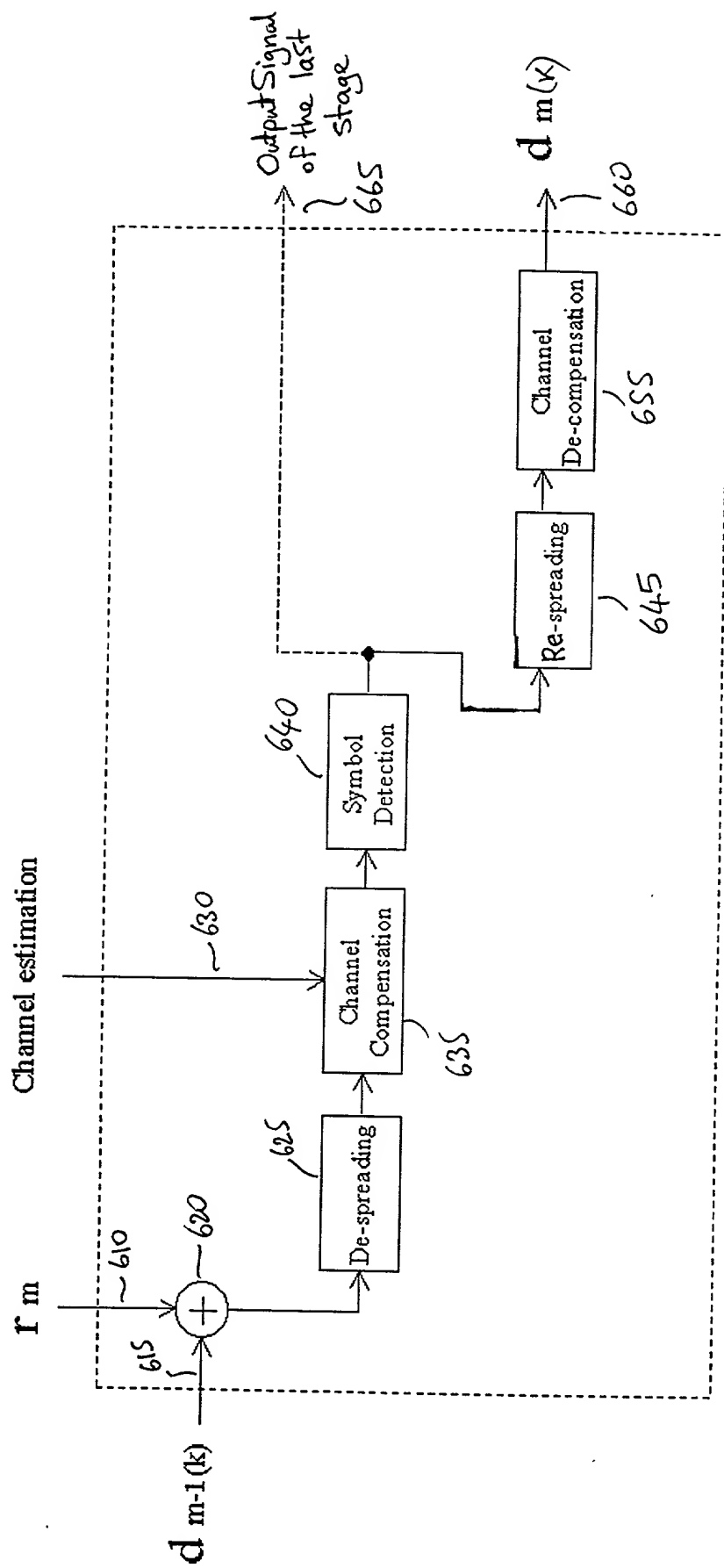


Fig 6